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SPADE: SPARSITY-ADAPTIVE EQUALIZATION FOR MMWAVE MASSIVE MU-MIMO

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ABSTRACT

We propose SParsity-ADaptive Equalization (SPADE), a novel approach to reduce the effective number of multiplications in sparse inner products by adaptively skipping multiplications that have little to no effect on the result. We apply SPADE to beamspace linear minimum mean square error (LMMSE) spatial equalization in all-digital millimeter-wave (mmWave) massive multiuser multiple-input multiple-output (MU-MIMO) systems. We propose a SPADE-based architecture that mutes insignificant multiplications to offer power savings. We use simulation results with line-of-sight (LoS) and non-LoS mmWave channel models to demonstrate that SPADE-LMMSE performs on par with state-of-the-art beamspace equalizers in terms of bit error-rate, while requiring significantly lower preprocessing complexity.

1. INTRODUCTION

Millimeter-wave (mmWave) communication [1,2] and massive multiuser (MU) multiple-input multiple-output (MIMO) [3] are two core technologies of fifth-generation (5G) and beyond 5G wireless systems. While mmWave communication provides access to large portions of unused bandwidth, it suffers from high propagation losses [4]. MU-MIMO [3] is able to (i) compensate for the high propagation losses via fine-grained beamforming and (ii) support simultaneous communication with multiple user equipments (UEs) in the same frequency band. However, the large number of basestation (BS) antennas combined with the high baseband sampling rates create new challenges for mmWave massive MU-MIMO hardware design.

While hybrid digital-analog architectures [5, 6] can reduce the hardware complexity, all-digital architectures with lowresolution data converters [7–9] and low-resolution baseband processing [10] achieve higher spectral efficiency, provide more flexibility, and simplify radio-frequency (RF) circuitry and baseband processing [11, 12]. However, to reduce the complexity and power consumption of baseband processing in all-digital BS designs, novel algorithms and architectures are necessary. Fortunately, mmWave channels typically consist of only a few dominant propagation paths [1, 4], which can be exploited to simplify BS design [13–18]. Concretely, by taking a spatial discrete Fourier transform (DFT) over the antenna array, which converts the antenna domain into beamspace domain, one can drastically simplify some of the most complex baseband processing tasks, including channel estimation [18] and spatial equalization [19–21]. Existing beamspace equalizers either suffer from a notable performance degradation compared to antenna domain spatial equalizers [20] or result in high preprocessing complexity [21].

1.1. Contributions

We propose SParsity-ADaptive Equalization (SPADE), a novel scheme to reduce the effective number of multiplications in sparse inner products. SPADE compares the entries of the two vectors and adaptively skips scalar multiplications which have negligible impact on the final result. When applied to beamspace linear minimum mean square error (LMMSE) equalization, SPADE offers significant reductions in the number of multiplications, without increasing the preprocessing complexity of LMMSE. We propose an architecture for SPADE-LMMSE that adaptively mutes multiplications to reduce power consumption. Furthermore, we use simulations with line-of-sight (LoS) and non-LoS mmWave channels to demonstrate that SPADE performs on par with the state-ofthe-art beamspace equalization algorithms.

1.2. Notation

Boldface lowercase and uppercase letters represent column vectors and matrices, respectively. For a matrix A, the transpose and Hermitian transpose are A^T and A^H , respectively, the *k*th column is denoted by a_k , and the Frobenius norm is $||A||_F$. For a vector a, the *k*th entry is denoted by *ak*, the real and imaginary parts are denoted by a^R and a^I , respectively, and the ℓ_{∞} -norm and ℓ_{∞} -norm are defined as $\|\mathbf{a}\|_{\infty} \triangleq \max_{k} |a_k|$ and $\|\mathbf{a}\|_{\infty} \triangleq \max\{\|\mathbf{a}^{\mathcal{R}}\|_{\infty}, \|\mathbf{a}^{\mathcal{I}}\|_{\infty}\},$ respectively [22]. The $N \times N$ identity and the unitary $N \times N$ discrete Fourier transform (DFT) matrices are denoted by I_N and F_N , respectively.

2. BACKGROUND

2.1. Antenna-Domain and Beamspace System Models

We focus on an all-digital mmWave massive MU-MIMO uplink system as depicted in Figure 1. We assume frequency-flat

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Fig. 1. Beamspace massive MU-MIMO uplink system with a sparsity-adaptive equalization (SPADE) receiver.

channels in which *U* single-antenna user equipments (UEs) transmit information simultaneously to a *B*-antenna basestation (BS) in the same frequency band. We model the *antenna domain* received signal vector $\bar{y} \in \mathbb{C}^B$ at the BS as $\bar{y} = \bar{H}s + \bar{n}$, where $\bar{H} \in \tilde{C}^{B \times U}$ is the uplink channel matrix, $\mathbf{s} \in \mathcal{S}^U$ is the vector of the UEs' data symbols taken from a discrete constellation set *S* (e.g., 16-QAM) with $E_s \triangleq \mathbb{E}[|s_u|^2],$ $u = 1, \ldots, U$. The noise vector $\bar{\mathbf{n}} \in \mathbb{C}^B$ has i.i.d. circularlysymmetric complex Gaussian entries with variance N_0 . The average receive signal-to-noise ratio (SNR) at the BS is defined as $SNR \triangleq E_s \|\bar{\mathbf{H}}\|_F^2/(BN_0)$.

In what follows, we consider mmWave propagation conditions with a *B*-antenna uniform linear array (ULA). To obtain the *beamspace* input-output relation, one applies a spatial DFT [23] to the received antenna-domain vector \bar{y} as follows:

$$
y = F_B \bar{y} = Hs + n. \tag{1}
$$

Here, $y = F_B \bar{y}$ is the beamspace receive vector, $H = F_B \bar{H}$ is the (typically sparse) beamspace channel matrix, and n is the beamspace noise vector with the same statistics as the antennadomain noise vector \bar{n} . Since the beamspace DFT must be computed for every receive vector \bar{y} , a practical system would rely on streaming fast Fourier transforms (FFTs) [23].

To see why the beamspace transform sparsifies mmWave channel matrices, consider the following plane-wave model for the antenna-domain channel vector \bar{h}_u of the *u*th UE [24]:

$$
\bar{\mathbf{h}}_u = \sum_{\ell=0}^{L-1} \alpha_\ell \bar{\mathbf{a}}(\phi_\ell). \tag{2}
$$

Here, *L* stands for the number of propagation paths, $\alpha_l \in \mathbb{C}$ is the channel gain of the ℓ th propagation path, and

$$
\bar{\mathbf{a}}(\phi_{\ell}) = \left[1, e^{j\phi_{\ell}}, e^{j2\phi_{\ell}}, \dots, e^{j(B-1)\phi_{\ell}}\right],\tag{3}
$$

where the spatial frequency ϕ_{ℓ} is determined by the ℓ th path's incident angle to the ULA. Since *L* is small for line-of-sight (LoS) mmWave channels, taking a DFT $h_u = F_B \overline{h}_u$ reveals the sparse structure of such channel vectors, i.e., most of the vector's energy is concentrated on the entries of h_u associated with the spatial frequencies ϕ_{ℓ} .

2.2. Beamspace LMMSE Equalization

Linear data detection consists of two phases: (i) *preprocessing*, which is performed only once per channel coherence interval

and produces a $U \times B$ equalization matrix W^H and (ii) *spatial equalization*, which is performed at baseband sampling rate (for each received signal vector y) in order to obtain estimates of the transmitted symbol vectors according to $\hat{s} = W^{H}y$. In what follows, we focus on beamspace LMMSE equalization for which the equalization matrix is given by

$$
\mathbf{W}^{\mathrm{H}} = \left(\mathbf{H}^{\mathrm{H}}\mathbf{H} + \frac{N_0}{E_s}\mathbf{I}_U\right)^{-1}\mathbf{H}^{\mathrm{H}}.
$$
 (4)

In order to support high-bandwidth communication at mmWave frequencies, the spatial equalization stage must be carried out at extremely high baseband sampling rates. Hence, to keep power consumption within reasonable bounds, efficient methods to calculate $\hat{\mathbf{s}} = \mathbf{W}^H \mathbf{y}$ must be deployed in practice.

2.3. Existing Sparsity-Exploiting Spatial Equalizers

In recent years, a number of sparsity-exploiting beamspace equalization methods have been proposed [19–21]. All of these methods exploit the fact that for sparse beamspace channel matrices H, the associated LMMSE equalization matri- $\cos W^H$ tend to be sparse as well. This observation enables design of beamspace equalization algorithms that produce sparse equalization matrices \tilde{W}^H with a given *density coefficient* $\delta \triangleq ||\tilde{\mathbf{W}}^H||_0 / (BU)$, where $||\tilde{\mathbf{W}}||_0$ is the number of nonzero entries of \tilde{W} . Such sparsity-exploiting spatial equalizers reduce the number of multiplications required when calculating $\hat{\mathbf{s}} = \tilde{\mathbf{W}}^H \mathbf{y}$, which reduces power consumption and/or implementation complexity. Among such methods, the entrywise orthogonal matching pursuit (EOMP) proposed in [21] achieves the highest sparsity (lowest δ) and hence highest complexity reduction during spatial equalization. EOMP and related algorithms, however, considerably increase the complexity of preprocessing, resulting in inefficient circuitry. We next propose SPADE-LMMSE, a novel sparsity-adaptive spatial equalization method that competes with EOMP in terms of complexity reduction during spatial equalization while directly using the conventional LMMSE equalization matrix (4) which means that the preprocessing complexity does not increase.

3. SPADE: SPARSITY-ADAPTIVE EQUALIZATION

Consider the inner product of two *B*-dimensional real-valued vectors $\langle \mathbf{w}, \mathbf{y} \rangle = \sum_{b=1}^{B} w_b y_b$. Intuitively, if *B* is large, then we can skip partial products *wby^b* of small magnitude, without incurring a large relative error in the result, assuming that the exact result is bounded away from zero. However, we cannot eliminate partial products based on their magnitude, as this requires actually performing the multiplication. Therefore, we propose to set thresholds for the absolute values of the two operands w_b and y_b , and skip (or mute) multiplications if the absolute values of both operands are below their respective thresholds. The same approach can be extended to complexvalued case, noting that each complex-valued inner product can be decomposed into four real-valued inner products. Since for mmWave channels, the rows \mathbf{w}_u^{H} , $u = 1, \ldots, U$ of beamspace

LMMSE matrices W^H in (4) and the receive vectors y exhibit sparsity, a large number of partial products when computing $\hat{s}_u = \mathbf{w}_u^H \mathbf{y}, u = 1, \dots, U$, will be small; hence, a large number of multiplications can be skipped—this is the general idea behind SPADE. In order to simplify hardware implementation of SPADE, we propose to use two fixed thresholds $\tau_y \in \mathbb{R}$ and $\tau_w \in \mathbb{R}$, for the real and imaginary parts of all entries of the receive vector y and the equalization matrix W^H , respectively.

3.1. Setting the SPADE Thresholds

The thresholds τ_y and τ_w are used to trade arithmetic precision for reduction in the effective number of multiplications. Setting these thresholds close to zero will result in high precision, but will increase the number of active multiplications. In contrast, setting these thresholds to large values will result in precision loss, but will lower the number of active multiplications. To improve the effectiveness of SPADE, we propose the following techniques:

1) Since we use a single threshold τ_y for all the entries of y, and the statistics of y change dynamically, we propose to determine a fixed threshold τ'_y and then set $\tau_y = ||\mathbf{y}|| \approx \tau'_y$ in order to incorporate fluctuations of y into τ_y .

2) Since we use a single threshold τ_w for all the entries of \mathbf{W}^H , we scale the rows \mathbf{w}_u^H , $u = 1, \dots, U$, so that their ℓ_{∞} -norms are equal. Such a row-scaling scheme was proposed in [10] to reduce the dynamic range of the entries of equalization matrices. For SPADE, we scale the rows of the LMMSE equalization matrix according to $V^H = \text{diag}(\alpha)W^H$ such that the rows v_u^H , $u = 1, \ldots, U$, of the scaled matrix V^H satisfy $\|\mathbf{v}_u\|_{\infty} < 1$. This can be accomplished by α_u = $1/(\|\mathbf{w}_{\mathbf{u}}^{\mathbf{H}}\|_{\infty} + \varepsilon)$, where $\varepsilon > 0$ is a small constant that ensures that $\|\mathbf{v}_u\|_{\tilde{\infty}}$ is just below one. With this approach, estimates of the transmit vector are computed as $\hat{\mathbf{s}} = \text{diag}(\boldsymbol{\alpha})^{-1} \mathbf{V}^{\text{H}} \mathbf{y}$, which corresponds to post-multiplying the *u*th entry of V^Hy by $1/\alpha_u$ for $u = 1, \ldots, U$. The threshold τ_w is applied to the entries of the scaled matrix VH.

3.2. SPADE-Based Architecture

Figure 2 shows a high-level architecture of a fully-unrolled beamspace LMMSE equalizer employing SPADE to adaptively mute multipliers. We emphasize that in high-bandwidth mmWave systems with multi-GHz baseband sampling rates, fully-unrolled architectures become a natural choice to deliver the desired throughput while minimizing data buffering and control overhead [23]. In Figure 2, the LMMSE preprocessing block receives beamspace channel estimates and computes V^H once per channel coherence interval. This block also performs the comparison of real and imaginary entries of V^H with τ_w and provides the comparison bits to the SPADE-based equalizer block. The FFT block takes the antenna domain received vectors \bar{y} and produces the beamspace domain vectors y, along with the comparison bits for each real and imaginary part of entries of y with τ_y . The fully-unrolled matrix-vector multiplication engine comprises *U* inner product engines, each consisting

Fig. 2. High-level architecture of a SPADE-LMMSE equalizer (top) with mutable multiplier (MM) details (bottom).

of *B* processing elements (PEs). Each PE is a complex-valued multiplier with four real-valued mutable multipliers (labeled as "MM"), whose internal architecture is depicted in Figure 2. At the beginning of each channel coherence interval, the weight registers of the PEs are loaded with the entries of V^H and the comparison bits provided by the LMMSE preprocessing block. Then, for each beamspace receive vector coming from the FFT block, each MM conditionally disables the registers before and after the multiplier if the comparison bits indicate that both operands have absolute values smaller than their thresholds. Consequently, such a matrix-vector multiplication engine adaptively saves power by nulling the switching activity of unused multipliers—this will reduce the dynamic power consumption. For correct functionality, the output of each muted multiplier must be set to zero, which is implemented by the AND gate at the output of each MM as shown in Figure 2. The number of muted multipliers depends on the sparsity of y and v_u^H , $u = 1, \ldots, U$, which is determined by the channel conditions. For example, as shown in Section 4, in a system with $B = 128$ BS antennas and $U = 16$ UEs with LoS channel conditions, it is possible to mute 80% of the multiplications while incurring no more than 0*.*1 dB SNR loss at an uncoded bit-error-rate (BER) of 1% .

We reiterate that LMMSE preprocessing is carried out only once per channel coherence interval and the spatial FFT can be implemented efficiently using the fully-unrolled multiplierless architecture proposed in [23]. However, the matrix-vector multiplication engine must operate constantly and at baseband

Fig. 3. Trade-off between SNR operating point at 1% uncoded BER and multiplier activity rate.

sampling rate, which emphasizes the importance of saving power consumption in the spatial equalization block.

4. SIMULATION RESULTS

To evaluate the performance-complexity trade-off offered by SPADE-LMMSE, we provide simulation results in Figure 3. We consider a mmWave massive MU-MIMO basestation with a $B = 128$ antenna $\lambda/2$ -spaced ULA and 16-QAM transmission from $U \in \{16, 32\}$ single-antenna UEs. We carried out Monte-Carlo simulations with LoS and non-LoS channels generated by the QuaDRiGa mmMAGIC UMi model [25] at a carrier frequency of 60 GHz. The UEs are placed randomly in a 120° sector within 10 m to 110 m from the BS array, with a minimum of 1° angular separation. We use power control so that the variation in receive power of UEs is limited to ± 3 dB. For channel estimation, we used pilot-based least squares (LS) followed by BEACHES [18]. We simulated the uncoded BER for (i) conventional antenna domain LMMSE equalization, (ii) EOMP-based beamspace equalization with density coefficient δ ranging from 10% to 90%, and (iii) beamspace SPADE-LMMSE with different values for the threshold pair (τ_y', τ_w) . The values of the threshold pair (τ_y', τ_w) corresponding to each simulated point of the SPADE-LMMSE curves in Figure 3 are obtained via a 2-dimensional grid search over a range of plausible candidates and picking the Pareto-optimal parameters.

4.1. Comparison with EOMP

Figure 3 shows the SNR operating point required to achieve an uncoded BER of 1% versus the fraction of active multipliers (which coincides with the density coefficient δ of EOMP). We see that the performance of SPADE-LMMSE is very close to EOMP, meaning that for a given fraction of active multipliers, they both achieve an uncoded BER of 1% at similar SNRs. We also see that SPADE-LMMSE and EOMP require fewer active multipliers for LoS channels than for non-LoS channels. For a negligible SNR loss (compared to antenna domain LMMSE), up to 80% of multipliers can be muted in a 128×16 LoS scenario, whereas about 50% can be muted for a non-LoS scenario. Note that the SNR operating point required to achieve an even lower uncoded BER of 0*.*1% behaves very similarly, but is omitted due to space constraints.

A key advantage of SPADE-LMMSE over EOMP is the fact that EOMP requires external control of the density coefficient δ dependent on the channel conditions to avoid a large SNR loss. In contrast, SPADE-LMMSE can simply use the same pair of threshold values (τ'_y, τ_w) while automatically adapting to the required number of multipliers based on the instantaneous channel conditions. For example, in the 128×16 LoS setting, choosing $\tau'_y = 1/2$ and $\tau_w = 1/20$ results in an SNR gap of only 0*.*25 dB with respect to antenna domain LMMSE equalization while requiring only 17% active multipliers. The same threshold parameters in the non-LoS setting results in an SNR gap of less than 0*.*15 dB while requiring only 45% active multipliers. Another key advantage is that SPADE simply uses conventional LMMSE preprocessing whereas the preprocessing complexity of EOMP is considerably higher. Using the complexity expressions provided in [21], the preprocessing complexity of EOMP—measured in terms of the number of real-valued multiplications—is about $15\times$ higher than that of conventional LMMSE for a $B = 128$ BS antenna and $U = 16$ UE system with density factor $\delta = 20\%$.

5. CONCLUSIONS

We have proposed SParsity-ADaptive Equalization (SPADE), a novel spatial equalization approach that adaptively reduces the number of multiplications based upon the instantaneous channel conditions. SPADE offers the following advantages: (i) the preprocessing complexity is the same as the conventional LMMSE equalization, (ii) the performance degradation with respect to antenna-domain equalization is negligible for suitably chosen thresholds, and (iii) the method adaptively reduces complexity and lowers power based on the instantaneous channel conditions. For LoS and non-LoS mmWave massive MU-MIMO channel models, we have demonstrated that SPADE performs on par with EOMP [21], but requires significantly lower preprocessing complexity. We emphasize that SPADE is not only suitable for spatial equalization in mmWave massive MU-MIMO systems, but finds potential use in many other applications that carry out approximate sparse matrix-vector products. A hardware implementation of the proposed SPADE-LMMSE is ongoing work.

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